

Application/Control No.	Applicant(s)/Patent under Reexamination
10/635,083	ADKISSON ET AL.
Examiner	Art Unit

2863

Toan M. Le

SEARCHED					
Class	Subclass	Date	Examiner		
702	186	6/20/2007	TL		
714	47-49	6/20/2007	TL		
714	1-3,25	6/20/2007	TL		
714	699,704	6/20/2007	TL		
714	706,709	6/20/2007	TL		
714	735-736	6/2/2007	TL		
712	224	6/20/2007	TL		
716	4	6/20/2007	TL		
Above	Updated	12/19/2007	TL		

INTERFERENCE SEARCHED						
Class	Subclass	Date	Examiner			
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SEARCH NOTES (INCLUDING SEARCH STRATEGY)				
٠.	DATE	EXMR		
702/186; 714/1-3,25,47- 49,699,704,706,709,735-736; 712/224; 716/4 (Counter Debug Match Threshold Event)	6/20/2007	TL		
East- See Search History Printout	6/20/2007	TL		
702/186; 714/1-3,25,47- 49,699,704,706,709,735-736; 712/224; 716/4 (Counter Debug Match Threshold Event Increment)	12/19/2007	. TL		
Counter Debug Match Threshold Compare Activate Event Signal Bits Bus Circuit Increment	12/19/2007	TL		
East- See Search History Printout	12/19/2007	TL		